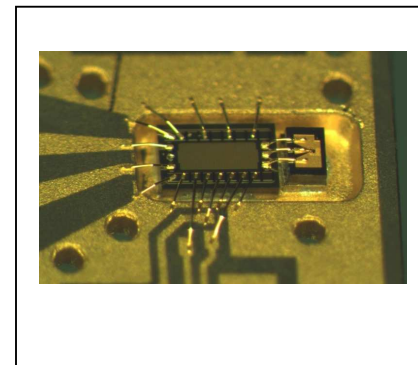


40 Gbit/s Receiver Testboard Subassembly (700-890nm)

Product Code: R40-850TB



Sample image only. Actual product may vary.

Product Description

The R40-850TB optical subassembly testboard utilizes a PIN photodetector and a transimpedance amplifier (TIA) chip assembled onto a customized ceramic board. The device is designed for ultra-high speed data communication test measurements of up to 40 Gbit/s.

Features

- capable up to 40 Gbit/s data rate
- proprietary PD and TIA
- high frequency package
- ceramic testboard

Applications

- Test of 40/100G short reach prototypes
- Fiber test application
- Research and development

Parameter	Typical (PD chips)	Notes
Operating Wavelength	700 to 890 nm	
3 dB Bandwidth	≥ 30 GHz	
Rise time (20% to 80%)	6 ps	
Responsivity	0.4 A/W	
Maximum input power	2 mW @ 850 nm	

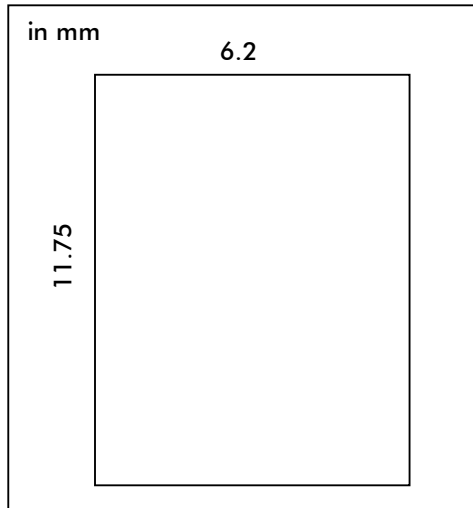
Datasheet

R40-850TB



Vertically Integrated Systems

Dimensions



Pinout

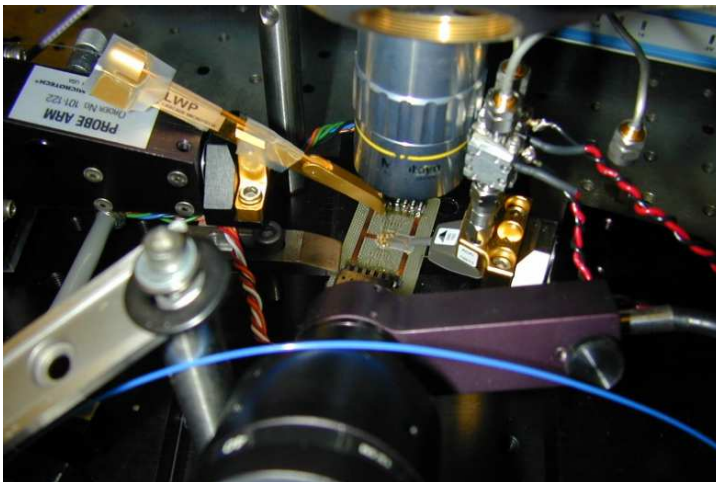
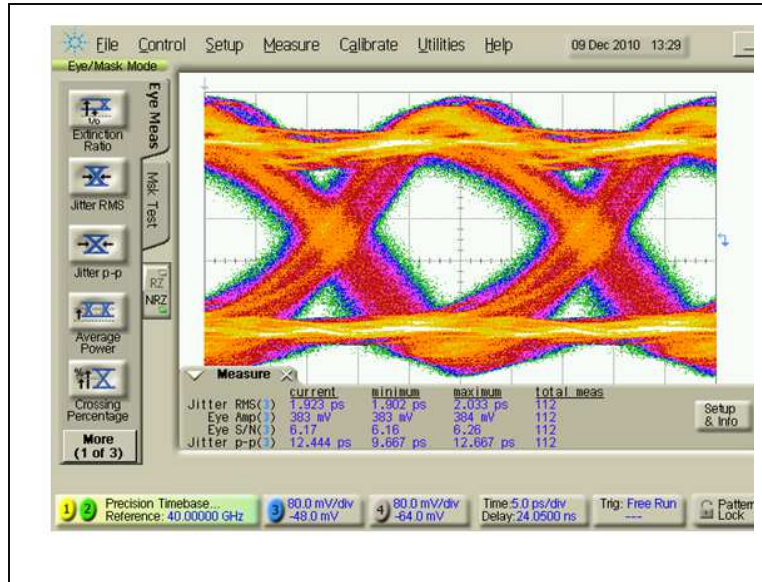
Pin #	Signal	Description
1	Vcc	supply voltage TIA
2	GND	optional
3	GND	optional
4	Vxing	cross point adjustment
5	Vmod	output signal adjustment
6	Vpd	supply voltage for PD
7	Vpd	diff. HF output
8	Vpd2	Diff. HF output

Product image



All product specifications and descriptions are subject to change without notice.
Please contact our sales department for additional information and to receive a quotation: sales@v-i-systems.com

Optical eye pattern at 40 Gbit/s



Test setup with RF microprobes and fiber alignment station.

Source: University of Georgia, USA, Georgia Tech, College of Engineering, School of Electrical and Computer Engineering, January 2012, IEEE 802 LAN/MAN Standards Committee

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