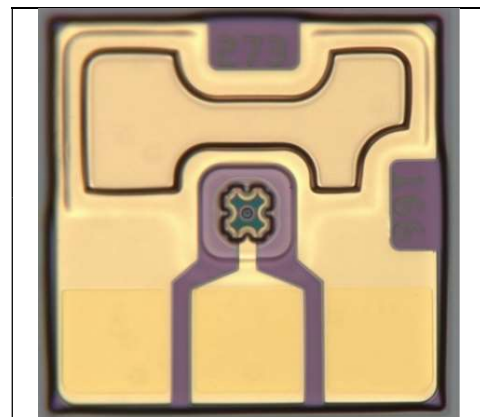


Up to 25GBaud modulation

Product Code: S-940-MC60
1x1 chip



Actual product may vary in appearance.

Product Description

These compact high power high modulation rate top-emitting 940nm GaAs-based vertical cavity surface emitting laser (VCSEL) chips for applications in sensing and wireless and fiber optical data communication

Features

- Chip size 250 x 250 μm
- Suitable for wire bonding GSG

Applications

- Sensing
- Optical data communication

Parameter	Typical	Notes
Emission wavelength	940 nm	range 935 – 945 nm
Data rate	Up to 25GBaud	
Threshold current	< 5 mA	
Peak output power	4 mW	

All product specifications and descriptions are subject to change without notice.

Please contact our sales department for additional information and to receive a quotation: sales@v-i-systems.com

Datasheet

S-940-MC60



Vertically Integrated Systems

Electro-optical characteristics (T = 0 to 85 °C)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Data rate	BR	NRZ		25	28	GBaud/s
Slope efficiency	η	20 mA		0.5	0.6	W/A
Threshold current	I_{th}			3	5	mA
Differential resistance	R_d	5-10 mA		40		Ω
Beam divergence	θ	FWFM (86%)	18	20	25	°
Output power	P_{max}	70mA		30	35	mW
Spectral bandwidth (RMS)	$\Delta\lambda_{RMS}$	70mA		0.5	1.0	nm

Absolute Maximum Ratings

Parameter	Symbol	Test Condition	Min	Max	Unit
Peak forward current	I_f			8	mA
Maximum reverse voltage	V_{rv}			5	V
Operating temperature	T_{op}			85	°C
Storage temperature	T_{st}		-40	100	°C
Soldering temperature	T_{sl}	max 260 sec		150	°C

Mechanical Dimensions

Parameter	Type	Min	Typ	Max	Unit
Length 1x1 VCSEL chip			210	250	μm
Height	All	140	150	160	μm
Width	All		210	250	μm

Dimensions

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www.v-i-systems.com

VI Systems GmbH Hardenbergstrasse 7 D-10623 Berlin

S-940-MC60



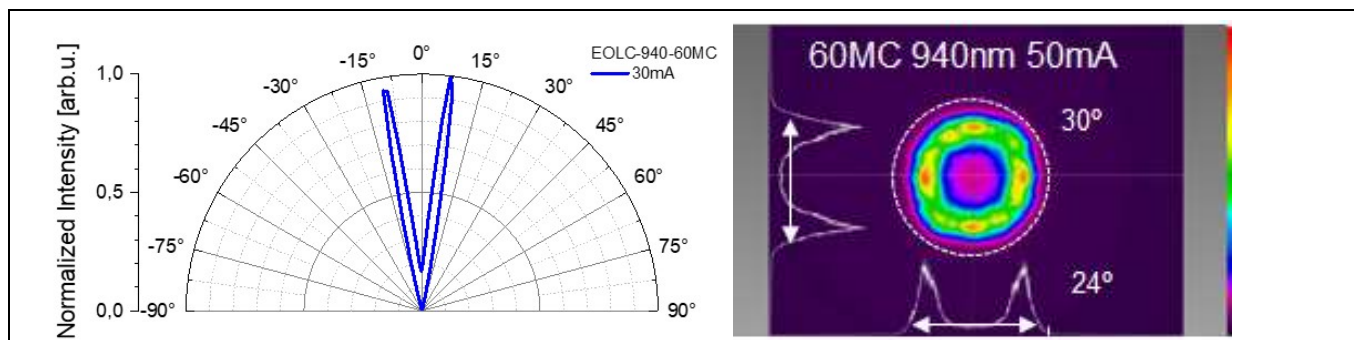
Datasheet

S-940-MC60



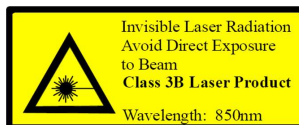
Vertically Integrated Systems

Far Field



Qualification Notification

S-940-MC60 chip series has been tested to meet specifications outlined in this data sheet at room temperature. However, it has not undergone qualification testing or characterization in the final version and therefore may not meet the performance specifications over all extremes.



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